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1. Document ID: US 6684438 B2

L19: Entry 1 of 25

File: USPT

Feb 3, 2004

US-PAT-NO: 6684438

DOCUMENT-IDENTIFIER: US 6684438 B2

TITLE: Method of using cache to determine the visibility to a remote database client of a plurality of database transactions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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2. Document ID: US 6510459 B2

L19: Entry 2 of 25

File: USPT

Jan 21, 2003

US-PAT-NO: 6510459

DOCUMENT-IDENTIFIER: US 6510459 B2

TITLE: Network image view server using efficient client-server, tiling and caching architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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3. Document ID: US 6505241 B2

L19: Entry 3 of 25

File: USPT

Jan 7, 2003

US-PAT-NO: 6505241

DOCUMENT-IDENTIFIER: US 6505241 B2

TITLE: Network intermediate node cache serving as proxy to client node to request missing data from server

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
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4. Document ID: US 6453343 B1

L19: Entry 4 of 25

File: USPT

Sep 17, 2002

US-PAT-NO: 6453343
DOCUMENT-IDENTIFIER: US 6453343 B1
** See image for Certificate of Correction **

TITLE: Methods, systems and computer program products for maintaining a common checkpoint cache for multiple sessions between a single client and server

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn D](#)

5. Document ID: US 6446089 B1

L19: Entry 5 of 25

File: USPT

Sep 3, 2002

US-PAT-NO: 6446089
DOCUMENT-IDENTIFIER: US 6446089 B1
** See image for Certificate of Correction **

TITLE: Method of using a cache to determine the visibility to a remote database client of a plurality of database transactions

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn D](#)

6. Document ID: US 6418544 B1

L19: Entry 6 of 25

File: USPT

Jul 9, 2002

US-PAT-NO: 6418544
DOCUMENT-IDENTIFIER: US 6418544 B1

TITLE: Use of a client meta-cache for realistic high-level web server stress testing with minimal client footprint

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn D](#)

7. Document ID: US 6374289 B1

L19: Entry 7 of 25

File: USPT

Apr 16, 2002

US-PAT-NO: 6374289
DOCUMENT-IDENTIFIER: US 6374289 B1

TITLE: Distributed client-based data caching system

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMC](#) [Drawn D](#)

8. Document ID: US 6314492 B1

L19: Entry 8 of 25

File: USPT

Nov 6, 2001

US-PAT-NO: 6314492

DOCUMENT-IDENTIFIER: US 6314492 B1

TITLE: System and method for server control of client cache

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

9. Document ID: US 6311216 B1

L19: Entry 9 of 25

File: USPT

Oct 30, 2001

US-PAT-NO: 6311216

DOCUMENT-IDENTIFIER: US 6311216 B1

**** See image for Certificate of Correction ****

TITLE: Method, computer program product, and system for client-side deterministic routing and URL lookup into a distributed cache of URLs

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

10. Document ID: US 6253282 B1

L19: Entry 10 of 25

File: USPT

Jun 26, 2001

US-PAT-NO: 6253282

DOCUMENT-IDENTIFIER: US 6253282 B1

TITLE: Object-oriented system, method and article of manufacture for a client-server with a client program cache

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

11. Document ID: US 6185617 B1

L19: Entry 11 of 25

File: USPT

Feb 6, 2001

US-PAT-NO: 6185617

DOCUMENT-IDENTIFIER: US 6185617 B1

TITLE: Construction and usage of a pre-warmed cache for client-server emulator

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

12. Document ID: US 6182127 B1

L19: Entry 12 of 25

File: USPT

Jan 30, 2001

US-PAT-NO: 6182127

DOCUMENT-IDENTIFIER: US 6182127 B1

**** See image for Certificate of Correction ****

- TITLE: Network image view server using efficient client-server tilting and caching architecture

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D](#)

13. Document ID: US 6178461 B1

L19: Entry 13 of 25

File: USPT

Jan 23, 2001

US-PAT-NO: 6178461

DOCUMENT-IDENTIFIER: US 6178461 B1

TITLE: Cache-based compaction technique for internet browsing using similar objects in client cache as reference objects

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D](#)

14. Document ID: US 6141737 A

L19: Entry 14 of 25

File: USPT

Oct 31, 2000

US-PAT-NO: 6141737

DOCUMENT-IDENTIFIER: US 6141737 A

TITLE: Method for dynamically and efficiently caching objects received from an application server by a client computer by subdividing cache memory blocks into equally-sized sub-blocks

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D](#)

15. Document ID: US 6138162 A

L19: Entry 15 of 25

File: USPT

Oct 24, 2000

US-PAT-NO: 6138162

DOCUMENT-IDENTIFIER: US 6138162 A

TITLE: Method and apparatus for configuring a client to redirect requests to a caching proxy server based on a category ID with the request

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw. D](#)

16. Document ID: US 6138141 A

L19: Entry 16 of 25

File: USPT

Oct 24, 2000

US-PAT-NO: 6138141

DOCUMENT-IDENTIFIER: US 6138141 A

** See image for Certificate of Correction **

- TITLE: Server to client cache protocol for improved web performance

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn De](#)

17. Document ID: US 6119126 A

L19: Entry 17 of 25

File: USPT

Sep 12, 2000

US-PAT-NO: 6119126

DOCUMENT-IDENTIFIER: US 6119126 A

TITLE: Object-relational query builder which determines existence of structures from information loaded from the server and cached locally on the client computing system

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn De](#)

18. Document ID: US 6026474 A

L19: Entry 18 of 25

File: USPT

Feb 15, 2000

US-PAT-NO: 6026474

DOCUMENT-IDENTIFIER: US 6026474 A

TITLE: Shared client-side web caching using globally addressable memory

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn De](#)

19. Document ID: US 6016520 A

L19: Entry 19 of 25

File: USPT

Jan 18, 2000

US-PAT-NO: 6016520

DOCUMENT-IDENTIFIER: US 6016520 A

TITLE: Method of viewing at a client viewing station a multiple media title stored at a server and containing a plurality of topics utilizing anticipatory caching

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn De](#)

20. Document ID: US 5991810 A

L19: Entry 20 of 25

File: USPT

Nov 23, 1999

US-PAT-NO: 5991810

DOCUMENT-IDENTIFIER: US 5991810 A

TITLE: User name authentication for gateway clients accessing a proxy cache server

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn D
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 21. Document ID: US 5826253 A

L19: Entry 21 of 25

File: USPT

Oct 20, 1998

US-PAT-NO: 5826253

DOCUMENT-IDENTIFIER: US 5826253 A

TITLE: Database system with methodology for notifying clients of any additions, deletions, or modifications occurring at the database server which affect validity of a range of data records cached in local memory buffers of clients

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn D
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 22. Document ID: US 5802297 A

L19: Entry 22 of 25

File: USPT

Sep 1, 1998

US-PAT-NO: 5802297

DOCUMENT-IDENTIFIER: US 5802297 A

TITLE: Client-server computer system and method utilizing a local client disk drive as a data cache

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn D
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 23. Document ID: US 5706435 A

L19: Entry 23 of 25

File: USPT

Jan 6, 1998

US-PAT-NO: 5706435

DOCUMENT-IDENTIFIER: US 5706435 A

TITLE: System for maintaining data coherency in cache memory by periodically broadcasting a single invalidation report from server to clients

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn D
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 24. Document ID: US 5617540 A

L19: Entry 24 of 25

File: USPT

Apr 1, 1997

US-PAT-NO: 5617540

DOCUMENT-IDENTIFIER: US 5617540 A

TITLE: System for binding host name of servers and address of available server in cache within client and for clearing cache prior to client establishes connection

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw](#) | [De](#)

25. Document ID: US 5581704 A

L19: Entry 25 of 25

File: USPT

Dec 3, 1996

US-PAT-NO: 5581704

DOCUMENT-IDENTIFIER: US 5581704 A

TITLE: System for maintaining data coherency in cache memory by periodically broadcasting invalidation reports from server to client

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Draw](#) | [De](#)

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1 Cache memory design for network processors

Tzi-Cker Chiueh; Pradhan, P.;

High-Performance Computer Architecture, 2000. HPCA-6. Proceedings. Sixth International Symposium on, 8-12 Jan. 2000
Pages:409 - 418

[\[Abstract\]](#) [\[PDF Full-Text \(112 KB\)\]](#) **IEEE CNF**

2 Routing prefix caching in network processor design

Huan Liu;

Computer Communications and Networks, 2001. Proceedings. Tenth International Conference on, 15-17 Oct. 2001
Pages:18 - 23

[\[Abstract\]](#) [\[PDF Full-Text \(83 KB\)\]](#) **IEEE CNF**

3 A neural network processor incorporating multiple on-chip cache memories

Tay, O.N.; Noakes, P.D.;

Neural Networks, 1991. 1991 IEEE International Joint Conference on, 18-21 1991

Pages:2222 - 2227 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(356 KB\)\]](#) **IEEE CNF**

4 Performance tradeoffs in multithreaded processors

Agarwal, A.;

Parallel and Distributed Systems, IEEE Transactions on, Volume: 3 , Issue: 5 , Sept. 1992

Pages:525 - 539

[\[Abstract\]](#) [\[PDF Full-Text \(1408 KB\)\]](#) [IEEE JNL](#)

5 Efficiency of remote access caches in future SMP-based CC-NUMA multiprocessors: initial results

Moreno, E.D.; Kofuji, S.T.;

Parallel Architectures, Algorithms, and Networks, 1997. (I-SPAN '97) Proceed Third International Symposium on , 18-20 Dec. 1997

Pages:190 - 197

[\[Abstract\]](#) [\[PDF Full-Text \(752 KB\)\]](#) [IEEE CNF](#)

6 Computation/communication balance-point modeling in multiproce

Hamacher, V.C.;

Communications, Computers and Signal Processing, 1999 IEEE Pacific Rim Conference on , 22-24 Aug. 1999

Pages:141 - 144

[\[Abstract\]](#) [\[PDF Full-Text \(380 KB\)\]](#) [IEEE CNF](#)

7 Cache memory design for Internet processors

Tzi-cker Chiueh; Pradham, P.;

Micro, IEEE , Volume: 20 , Issue: 1 , Jan.-Feb. 2000

Pages:28 - 33

[\[Abstract\]](#) [\[PDF Full-Text \(76 KB\)\]](#) [IEEE JNL](#)

8 The performance value of shared network caches in clustered multiprocessor workstations

Bennett, J.K.; Fletcher, K.E.; Speight, W.E.;

Distributed Computing Systems, 1996., Proceedings of the 16th International Conference on , 27-30 May 1996

Pages:64 - 74

[\[Abstract\]](#) [\[PDF Full-Text \(1080 KB\)\]](#) [IEEE CNF](#)

9 Lynx/Galactica Net: a distributed, cache coherent multiprocessing system

Wilson, A.; Teller, M.; Probert, T.; Dyung Le; LaRowe, R.;

System Sciences, 1992. Proceedings of the Twenty-Fifth Hawaii International Conference on , Volume: i , 7-10 Jan. 1992

Pages:416 - 426 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(932 KB\)\]](#) [IEEE CNF](#)

10 Design issues for high-performance active routers

Wolf, T.; Turner, J.S.;

Selected Areas in Communications, IEEE Journal on , Volume: 19 , Issue: 3 , 2001

Pages:404 - 409

[\[Abstract\]](#) [\[PDF Full-Text \(204 KB\)\]](#) [IEEE JNL](#)

11 A case study of trace-driven simulation for analyzing interconnect networks: cc-NUMAs with ILP processors

Puente, V.; Prellezo, J.M.; Izu, C.; Gregorio, J.A.; Beivide, R.;
Parallel and Distributed Processing, 2000. Proceedings. 8th Euromicro Workst
on , 19-21 Jan. 2000
Pages:174 - 180

[\[Abstract\]](#) [\[PDF Full-Text \(244 KB\)\]](#) [IEEE CNF](#)

12 CommBench-a telecommunications benchmark for network proces

Wolf, T.; Franklin, M.;
Performance Analysis of Systems and Software, 2000. ISPASS. 2000 IEEE
International Symposium on , 24-25 April 2000
Pages:154 - 162

[\[Abstract\]](#) [\[PDF Full-Text \(440 KB\)\]](#) [IEEE CNF](#)

13 SOME-Bus-NOW: a Network of Wrkstations with broadcast

Katsinis, C.; Hecht, D.;
Network Computing and Applications, 2003. NCA 2003. Second IEEE Internat
Symposium on , 16-18 April 2003
Pages:113 - 120

[\[Abstract\]](#) [\[PDF Full-Text \(602 KB\)\]](#) [IEEE CNF](#)

14 Delayed consistency and its effects on the interconnection network shared memory multiprocessors

Ghun Choe; Chong Sang Kim;
TENCON '94. IEEE Region 10's Ninth Annual International Conference. Theme
'Frontiers of Computer Technology'. Proceedings of 1994 , 22-26 Aug. 1994
Pages:436 - 439 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) [IEEE CNF](#)

15 Pica: An ultra-light processor for high-throughput applications

Wills, D.S.; Lacy, W.S.; Cat, H.; Hopper, M.A.; Razdan, A.; Chai, S.M.;
Computer Design: VLSI in Computers and Processors, 1993. ICCD '93.
Proceedings., 1993 IEEE International Conference on , 3-6 Oct. 1993
Pages:410 - 414

[\[Abstract\]](#) [\[PDF Full-Text \(504 KB\)\]](#) [IEEE CNF](#)

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